

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:	)	
Shunpei YAMAZAKI	)	Examiner: Karla A. Moore
Serial No. 10/772,267	)	Group Art Unit: 1716
Filed: February 6, 2004	)	Confirmation No.: 4444
For: SEMICONDUCTOR MANUFACTURING DEVICE	)	

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

JP 2003-031477 was cited in the counterpart Japanese Application No. 2004-564064 on September 14, 2010.

Applicants wish to bring to the Examiner's attention the following family information. U.S. Patent Nos. 6,697,698 and 6,801,827 are in the same family as Japanese Patent No. JP 2003-031477, but these family patents were not cited in the foreign communication noted above.

The undersigned certifies (with the exception of family documents noted above) that each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in connection with a counterpart foreign application not more than three (3) months prior to the filing of this statement.

The Commissioner is hereby authorized to charge \$180.00 to comply with the provisions of 37 C.F.R. § 1.97(c), and any other fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

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